|  |  |  |
| --- | --- | --- |
| **SN** | **RISC** | **CISC** |
| 1 | Reduced Instruction Set Computer | Complex Instruction Set Computer |
| 2 | Heavy use of RAM | Efficient use of RAM |
| 3 | Limited addressing mode | More addressing mode |
| 4 | Single instruction cycle instructions | Multi cycle instructions |
| 5 | More registers | Less registers |
| 6 | Emphasis on hardware | Emphasis on software |
| 7 | Fixed length instruction size | Variable length instruction size |
| 8 | Pipelining is easy | Pipelining is difficult |